

**REVISED B.TECH VI SEMESTER – [R20] REGULAR END EXAMINATIONS JUNE-2024
TIME-TABLE**

BRANCH	20-06-2024	22-06-2024	25-06-2024	27-06-2024	29-06-2024
	10.00 AM TO 1.00 PM	10.00 AM TO 1.00 PM	10.00 AM TO 1.00 PM	10.00 AM TO 1.00 PM	10.00 AM TO 1.00 PM
ELECTRONICS & COMMUNICATIONS ENGINEERING (ECE-04)	VLSI DESIGN	DIGITAL SIGNAL PROCESSING	EMBEDDED SYSTEM DESIGN	BASICS OF ENTREPRENEURSHIP	MICROWAVE ENGINEERING
INFORMATION TECHNOLOGY (IT-12)	COMPILER DESIGN	BUSINESS ECONOMICS & FINANCIAL ANALYSIS	SOFTWARE TESTING METHODOLOGIES	FUNDAMENTALS OF INTERNET OF THINGS	MACHINE LEARNING
COMPUTER SCIENCE AND ENGINEERING -DS (CSD-67)	COMPILER DESIGN	SOFTWARE ENGINEERING	SOFTWARE TESTING METHODOLOGIES	INTRODUCTION TO ARTIFICIAL INTELLIGENCE	DATA SCIENCE
COMPUTER SCIENCE AND DESIGN (CSG-74)	COMPILER DESIGN	SOFTWARE ENGINEERING	SOFTWARE TESTING METHODOLOGIES	INTRODUCTION TO ARTIFICIAL INTELLIGENCE	DATA SCIENCE
COMPUTER SCIENCE & ENGINEERING (CSE-05)	COMPILER DESIGN	BUSINESS ECONOMICS & FINANCIAL ANALYSIS	SOFTWARE TESTING METHODOLOGIES	FUNDAMENTALS OF INTERNET OF THINGS	MACHINE LEARNING
COMPUTER SCIENCE AND ENGINEERING -AIML (CSM-66)	COMPILER DESIGN	SOFTWARE ENGINEERING	SOFTWARE TESTING METHODOLOGIES	INTRODUCTION TO DATA SCIENCE	ARTIFICIAL INTELLIGENCE
ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING (AIM-73)	COMPILER DESIGN	SOFTWARE ENGINEERING	SOFTWARE TESTING METHODOLOGIES	INTRODUCTION TO DATA SCIENCE	ARTIFICIAL INTELLIGENCE

DATE:11-06-2024


CONTROLLER OF EXAMINATIONS


DIRECTOR