

Ref: CMRTC/E-7/BoS/CSE/MoM/06





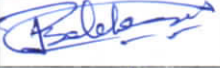



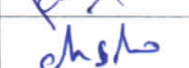
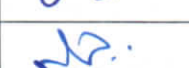
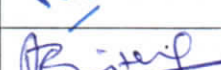

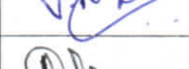

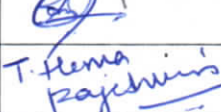
Date: 23/08/2025

BOARD OF STUDIES – COMPUTER SCIENCE AND ENGINEERING

MINUTES OF MEETING

The 6th Board of Studies meeting for the R25 UG Programme (B. Tech - CSE) and R25 PG Programme (M. Tech - CSE) was convened on 23rd August 2025 at 10:00 AM through Google Meet (<https://meet.google.com/zry-vvhe-soh>). The meeting was conducted virtually, and the proceedings have been duly recorded and preserved for reference.

The following members attended the meeting:

S. NO.	Name	Designation	Role	Signature
1	Dr. A. Raji Reddy	Director, CMR Technical Campus	Special Invitee	
2	Dr. N Bhaskar	Professor & Head, Department of CSE, CMRTC	Chairman	
3	Dr. K. P. Supreethi	Professor of CSE, JNTUH, UCESTH	University Nominated Member	
4	Sri. C. Abhinav Reddy	Vice Chairman, CMR Technical Campus	Subject Expert	
5	Mr. Bala Prasad Peddigari	Chief Innovation Officer, CMT Unit, TCS Limited	Industry Representative	
6	Dr. Siva Skandha Sanagala	Professor & HOD of CSE, CMRCET	External Subject Expert	
7	Dr. D.T.V Dharmajee Rao	Professor & Dean Academics, CMRTC	Subject Expert	
8	Dr. K. Srujan Raju	Professor & Dean R&D, CMRTC	Subject Expert	
9	Dr. S Rao Chintalapudi	Professor & Head, Department of CSE (AI & ML), CMRTC	Subject Expert	
10	Dr. K. Murali	Professor & Head, Department of CSE(DS), CMRTC	Subject Expert	
11	Dr. B. Kavitha Rani	Professor & Head, Department of IT, CMRTC	Subject Expert	
12	Dr. V Naresh Kumar	Associate Professor, Department of CSE, CMRTC	Subject Expert	
13	Dr. K. Maheswari	Associate Professor, Department of CSE, CMRTC	Subject Expert	
14	Dr. B. Laxmaiah	Associate Professor, Department of CSE, CMRTC	Subject Expert	
15	Toram Hema Rajeswari (167R1A05P4)	Senior Software Engineer, Value Labs	Alumnus	

Agenda of the Meeting:

1. Review and approval of the R25 UG Programme (B.Tech – CSE) Four Years Course Structure and I and II-Year Syllabus.
2. Review and approval of the R25 PG Programme (M.Tech – CSE) Course Structure and Syllabus.
3. Suggestions and modifications from BoS members.
4. Any other points with the permission of the Chair.

Proceedings:

- Dr. Nuthanakanti Bhaskar, Professor of CSE and Chairman, Board of Studies, welcomed all the members to the 6th BoS meeting and initiated the discussions.
- The committee deliberated on the R25 B.Tech CSE four year course structure, with a detailed review of the I and II Year syllabus.
- The M. Tech CSE course structure and syllabus were also presented and discussed in detail.
- Members provided constructive feedback and suggestions for further refinement of both UG and PG programmes.

Resolutions Taken:

R25 B.Tech CSE – Four Year Course Structure

The Board reviewed the proposed R25 B.Tech CSE course structure and approved it with the following suggestions:

1. Course Objectives and Course Outcomes were revised for selected courses.
2. The course Automata and Compiler Design in V Semester was separated into two independent courses:
 - a. Formal Languages and Automata Theory.
 - b. Compiler Design.
3. DevOps Theory course was removed from the curriculum, and Formal Languages and Automata Theory was introduced in V Semester, since DevOps Lab already covers the required topics.
4. Compiler Design Theory and Compiler Design Lab were moved to VII Semester.
5. The DevOps Lab and English for Employability Skills Lab were swapped between semesters to ensure balanced workload distribution.
6. Deep Learning Theory and Deep Learning Lab were moved from VI Semester to V Semester.

7. Natural Language Processing Theory and Lab were moved from VII Semester to VI Semester, in view of student placement requirements.

I & II Year B.Tech CSE Syllabus

The syllabus for the first and second years of the R25 B.Tech CSE programme was approved with the following recommendations:

1. In Object Oriented Programming through Java, the concepts of String classes and Object class were removed from Unit-1, as they are already included in Unit-4.
2. In Computer Networks, certain units were reorganized to follow the bottom-up OSI model approach.
3. In Algorithm Design and Analysis, some concepts were swapped to ensure completion of the syllabus within prescribed hours.
4. Open Elective subject titles and syllabus were revised in consideration of relevance to all other departments.
5. The committee strongly recommended encouraging students to complete MOOC certifications and add-on courses through platforms such as NPTEL, IIT Bombay Spoken Tutorial, EduSkills, Infosys Springboard, etc.

R25 M.Tech CSE Course Structure and Syllabus

The Board discussed the M.Tech CSE programme course structure and syllabus and approved it with the recommendation to:

1. Adopt the M.Tech CSE course structure and syllabus prescribed by JNTU Hyderabad for R25.

Conclusion:

The Chairman, Dr. Nuthanakanti Bhaskar, Professor of CSE, expressed gratitude to all the Board of Studies members for their valuable suggestions and active participation. The members appreciated the efforts of the Department in designing the R25 UG and PG curriculum to meet industry and academic requirements. The BoS members also conveyed their appreciation and satisfaction with the courses and activities of the Department.

The meeting concluded with a vote of thanks to all members and the proceedings have been duly recorded and preserved for reference.



Chairman, BoS
Department of CSE